



TriMag IV

Secure Magstripe Decoding ASIC.

Triple track MSR decoding ASIC with a 36-pin component and small footprint. The ASIC takes incoming MSR data and encrypts it with the built-in Triple DES engine providing maximum data security.

Key Features

- ✓ PCI SRED compliant security protection
- ✓ circuits (physical tamper detect, over/under voltage, over/under temperature)
- ✓ Compact 5mm x 5mm, 36-pin component
- ✓ Supports up to 3 tracks of card reading simultaneously
- ✓ EMV compliant ISO 7816 smart card interface
- ✓ Low power mode

Contact Us for More Information on our Payment Solutions

- (714) 761-6368
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10721 Walker Street
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- ISO 9001 Certified
ISO 14001 Certified



Electrical

Encryption Hardware encryption engine TDES

Interface Audio jack, SPI, UART, 13 general purpose I/Os (GPIO) with 4mA push-pull strength, 3 general purpose I/Os with 12mA push-pull strength for LED & beeper

Key Management DUKPT

Security Protection Circuits PCI SRED compliant, physical tamper detect, over/under voltage, over/under temperature



Environmental

Low Power Mode sleep mode 35 μ A, shutdown mode with tamper on <3 μ A, battery ship mode <2 μ A

Moisture Sensitivity Level 3

Mechanical

Dimensions 5 mm x 5 mm

Memory 96KB code, 6KB RAM, EEPROM

MCU 32MHz 32bit ARM Cortex MO

Smart Card ISO 7816 UART

Miscellaneous Random number generator, real time clock

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